



reg.12.server.1.expires.overlap="8" reg.12.server.2.expires.overlap="8"  
reg.13.server.1.expires.overlap="8" reg.13.server.2.expires.overlap="8"  
reg.14.server.1.expires.overlap="8" reg.14.server.2.expires.overlap="8"  
reg.15.server.1.expires.overlap="8" reg.15.server.2.expires.overlap="8"  
reg.16.server.1.expires.overlap="8" reg.16.server.2.expires.overlap="8"  
reg.17.server.1.expires.overlap="8" reg.17.server.2.expires.overlap="8"  
reg.18.server.1.expires.overlap="8" reg.18.server.2.expires.overlap="8"  
reg.19.server.1.expires.overlap="8" reg.19.server.2.expires.overlap="8"  
reg.20.server.1.expires.overlap="8" reg.20.server.2.expires.overlap="8"  
reg.1.server.1.failOver.failBack.mode="newRequests"  
reg.1.server.2.failOver.failBack.mode="newRequests"  
reg.2.server.1.failOver.failBack.mode="newRequests"  
reg.2.server.2.failOver.failBack.mode="newRequests"  
reg.3.server.1.failOver.failBack.mode="newRequests"  
reg.3.server.2.failOver.failBack.mode="newRequests"  
reg.4.server.1.failOver.failBack.mode="newRequests"  
reg.4.server.2.failOver.failBack.mode="newRequests"  
reg.5.server.1.failOver.failBack.mode="newRequests"  
reg.5.server.2.failOver.failBack.mode="newRequests"  
reg.6.server.1.failOver.failBack.mode="newRequests"  
reg.6.server.2.failOver.failBack.mode="newRequests"  
reg.7.server.1.failOver.failBack.mode="newRequests"  
reg.7.server.2.failOver.failBack.mode="newRequests"  
reg.8.server.1.failOver.failBack.mode="newRequests"  
reg.8.server.2.failOver.failBack.mode="newRequests"  
reg.9.server.1.failOver.failBack.mode="newRequests"  
reg.9.server.2.failOver.failBack.mode="newRequests"  
reg.10.server.1.failOver.failBack.mode="newRequests"  
reg.10.server.2.failOver.failBack.mode="newRequests"  
reg.11.server.1.failOver.failBack.mode="newRequests"  
reg.11.server.2.failOver.failBack.mode="newRequests"  
reg.12.server.1.failOver.failBack.mode="newRequests"  
reg.12.server.2.failOver.failBack.mode="newRequests"  
reg.13.server.1.failOver.failBack.mode="newRequests"  
reg.13.server.2.failOver.failBack.mode="newRequests"  
reg.14.server.1.failOver.failBack.mode="newRequests"  
reg.14.server.2.failOver.failBack.mode="newRequests"  
reg.15.server.1.failOver.failBack.mode="newRequests"  
reg.15.server.2.failOver.failBack.mode="newRequests"  
reg.16.server.1.failOver.failBack.mode="newRequests"  
reg.16.server.2.failOver.failBack.mode="newRequests"  
reg.17.server.1.failOver.failBack.mode="newRequests"



reg.16.server.2.failOver.failRegistrationOn="1" reg.17.server.1.failOver.failRegistrationOn="1"  
reg.17.server.2.failOver.failRegistrationOn="1" reg.18.server.1.failOver.failRegistrationOn="1"  
reg.18.server.2.failOver.failRegistrationOn="1" reg.19.server.1.failOver.failRegistrationOn="1"  
reg.19.server.2.failOver.failRegistrationOn="1" reg.20.server.1.failOver.failRegistrationOn="1"  
reg.20.server.2.failOver.failRegistrationOn="1" reg.1.server.1.failOver.onlySignalWithRegistered="1"  
reg.1.server.2.failOver.onlySignalWithRegistered="1"  
reg.2.server.1.failOver.onlySignalWithRegistered="1"  
reg.2.server.2.failOver.onlySignalWithRegistered="1"  
reg.3.server.1.failOver.onlySignalWithRegistered="1"  
reg.3.server.2.failOver.onlySignalWithRegistered="1"  
reg.4.server.1.failOver.onlySignalWithRegistered="1"  
reg.4.server.2.failOver.onlySignalWithRegistered="1"  
reg.5.server.1.failOver.onlySignalWithRegistered="1"  
reg.5.server.2.failOver.onlySignalWithRegistered="1"  
reg.6.server.1.failOver.onlySignalWithRegistered="1"  
reg.6.server.2.failOver.onlySignalWithRegistered="1"  
reg.7.server.1.failOver.onlySignalWithRegistered="1"  
reg.7.server.2.failOver.onlySignalWithRegistered="1"  
reg.8.server.1.failOver.onlySignalWithRegistered="1"  
reg.8.server.2.failOver.onlySignalWithRegistered="1"  
reg.9.server.1.failOver.onlySignalWithRegistered="1"  
reg.9.server.2.failOver.onlySignalWithRegistered="1"  
reg.10.server.1.failOver.onlySignalWithRegistered="1"  
reg.10.server.2.failOver.onlySignalWithRegistered="1"  
reg.11.server.1.failOver.onlySignalWithRegistered="1"  
reg.11.server.2.failOver.onlySignalWithRegistered="1"  
reg.12.server.1.failOver.onlySignalWithRegistered="1"  
reg.12.server.2.failOver.onlySignalWithRegistered="1"  
reg.13.server.1.failOver.onlySignalWithRegistered="1"  
reg.13.server.2.failOver.onlySignalWithRegistered="1"  
reg.14.server.1.failOver.onlySignalWithRegistered="1"  
reg.14.server.2.failOver.onlySignalWithRegistered="1"  
reg.15.server.1.failOver.onlySignalWithRegistered="1"  
reg.15.server.2.failOver.onlySignalWithRegistered="1"  
reg.16.server.1.failOver.onlySignalWithRegistered="1"  
reg.16.server.2.failOver.onlySignalWithRegistered="1"  
reg.17.server.1.failOver.onlySignalWithRegistered="1"  
reg.17.server.2.failOver.onlySignalWithRegistered="1"  
reg.18.server.1.failOver.onlySignalWithRegistered="1"  
reg.18.server.2.failOver.onlySignalWithRegistered="1"  
reg.19.server.1.failOver.onlySignalWithRegistered="1"  
reg.19.server.2.failOver.onlySignalWithRegistered="1"

reg.20.server.1.failOver.onlySignalWithRegistered="1"  
reg.20.server.2.failOver.onlySignalWithRegistered="1" reg.1.server.1.failOver.reRegisterOn="0"  
reg.1.server.2.failOver.reRegisterOn="0" reg.2.server.1.failOver.reRegisterOn="0"  
reg.2.server.2.failOver.reRegisterOn="0" reg.3.server.1.failOver.reRegisterOn="0"  
reg.3.server.2.failOver.reRegisterOn="0" reg.4.server.1.failOver.reRegisterOn="0"  
reg.4.server.2.failOver.reRegisterOn="0" reg.5.server.1.failOver.reRegisterOn="0"  
reg.5.server.2.failOver.reRegisterOn="0" reg.6.server.1.failOver.reRegisterOn="0"  
reg.6.server.2.failOver.reRegisterOn="0" reg.7.server.1.failOver.reRegisterOn="0"  
reg.7.server.2.failOver.reRegisterOn="0" reg.8.server.1.failOver.reRegisterOn="0"  
reg.8.server.2.failOver.reRegisterOn="0" reg.9.server.1.failOver.reRegisterOn="0"  
reg.9.server.2.failOver.reRegisterOn="0" reg.10.server.1.failOver.reRegisterOn="0"  
reg.10.server.2.failOver.reRegisterOn="0" reg.11.server.1.failOver.reRegisterOn="0"  
reg.11.server.2.failOver.reRegisterOn="0" reg.12.server.1.failOver.reRegisterOn="0"  
reg.12.server.2.failOver.reRegisterOn="0" reg.13.server.1.failOver.reRegisterOn="0"  
reg.13.server.2.failOver.reRegisterOn="0" reg.14.server.1.failOver.reRegisterOn="0"  
reg.14.server.2.failOver.reRegisterOn="0" reg.15.server.1.failOver.reRegisterOn="0"  
reg.15.server.2.failOver.reRegisterOn="0" reg.16.server.1.failOver.reRegisterOn="0"  
reg.16.server.2.failOver.reRegisterOn="0" reg.17.server.1.failOver.reRegisterOn="0"  
reg.17.server.2.failOver.reRegisterOn="0" reg.18.server.1.failOver.reRegisterOn="0"  
reg.18.server.2.failOver.reRegisterOn="0" reg.19.server.1.failOver.reRegisterOn="0"  
reg.19.server.2.failOver.reRegisterOn="0" reg.20.server.1.failOver.reRegisterOn="0"  
reg.20.server.2.failOver.reRegisterOn="0" reg.1.server.1.lcs="0" reg.1.server.2.lcs="0"  
reg.2.server.1.lcs="0" reg.2.server.2.lcs="0" reg.3.server.1.lcs="0" reg.3.server.2.lcs="0"  
reg.4.server.1.lcs="0" reg.4.server.2.lcs="0" reg.5.server.1.lcs="0" reg.5.server.2.lcs="0"  
reg.6.server.1.lcs="0" reg.6.server.2.lcs="0" reg.7.server.1.lcs="0" reg.7.server.2.lcs="0"  
reg.8.server.1.lcs="0" reg.8.server.2.lcs="0" reg.9.server.1.lcs="0" reg.9.server.2.lcs="0"  
reg.10.server.1.lcs="0" reg.10.server.2.lcs="0" reg.11.server.1.lcs="0" reg.11.server.2.lcs="0"  
reg.12.server.1.lcs="0" reg.12.server.2.lcs="0" reg.13.server.1.lcs="0" reg.13.server.2.lcs="0"  
reg.14.server.1.lcs="0" reg.14.server.2.lcs="0" reg.15.server.1.lcs="0" reg.15.server.2.lcs="0"  
reg.16.server.1.lcs="0" reg.16.server.2.lcs="0" reg.17.server.1.lcs="0" reg.17.server.2.lcs="0"  
reg.18.server.1.lcs="0" reg.18.server.2.lcs="0" reg.19.server.1.lcs="0" reg.19.server.2.lcs="0"  
reg.20.server.1.lcs="0" reg.20.server.2.lcs="0" reg.1.server.1.port="0" reg.1.server.2.port="0"  
reg.2.server.1.port="0" reg.2.server.2.port="0" reg.3.server.1.port="0" reg.3.server.2.port="0"  
reg.4.server.1.port="0" reg.4.server.2.port="0" reg.5.server.1.port="0" reg.5.server.2.port="0"  
reg.6.server.1.port="0" reg.6.server.2.port="0" reg.7.server.1.port="0" reg.7.server.2.port="0"  
reg.8.server.1.port="0" reg.8.server.2.port="0" reg.9.server.1.port="0" reg.9.server.2.port="0"  
reg.10.server.1.port="0" reg.10.server.2.port="0" reg.11.server.1.port="0" reg.11.server.2.port="0"  
reg.12.server.1.port="0" reg.12.server.2.port="0" reg.13.server.1.port="0" reg.13.server.2.port="0"  
reg.14.server.1.port="0" reg.14.server.2.port="0" reg.15.server.1.port="0" reg.15.server.2.port="0"  
reg.16.server.1.port="0" reg.16.server.2.port="0" reg.17.server.1.port="0" reg.17.server.2.port="0"  
reg.18.server.1.port="0" reg.18.server.2.port="0" reg.19.server.1.port="0" reg.19.server.2.port="0"  
reg.20.server.1.port="0" reg.20.server.2.port="0" reg.1.server.1.register="1" reg.1.server.2.register="1"



